

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,373,090 B2
APPLICATION NO. : 10/814911
DATED : May 13, 2008
INVENTOR(S) : Kazemi-Nia

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page.

The title page showing an illustrative figure should be deleted and substitute therefor the attached Title Page.

Drawings

On Sheet 1 of 8, in Fig. 1 (Referral Numeral 110), line 1, delete "Tranceiver" and insert -- Transceiver --, therefor.

Signed and Sealed this

Sixteenth Day of September, 2008

A handwritten signature in black ink, reading "Jon W. Dudas". The signature is stylized, with a large loop for the "J" and a cursive "Dudas".

JON W. DUDAS
Director of the United States Patent and Trademark Office

(12) **United States Patent**
Kazemi-Nia et al.

(10) **Patent No.:** **US 7,373,090 B2**
(45) Date of Patent: **May 13, 2008**

(54) **MODULATOR DRIVER CIRCUIT WITH
SELECTABLE ON-CHIP TERMINATION**

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(*) **Notice:** Subject to any disclaimer, the term of this
patent is extended or adjusted under 35
U.S.C. 154(b) by 677 days.

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H04B 10/04 (2006.01)

(52) **U.S. Cl.** 398/198; 359/245; 359/248

(58) **Field of Classification Search** 398/198;
327/108; 359/245, 248, 237, 254, 276, 278,
359/279; 385/2

See application file for complete search history.

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(57) **ABSTRACT**

A method and apparatus to accommodate differing output
loads without sacrificing impedance matching in an optical
modulator driver.

14 Claims, 8 Drawing Sheets

